

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Error
1	BRS	L1	1068 488	Photo or sensor or sensing or sensed or diode or photosensor	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 11:25			0
2	BRS	L2	3214 834	impurity or dop\$4 or mix\$4 or blend\$4 or commix\$4 or immix\$4 or intermix\$4	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 11:27			0
3	BRS	L3	1179 9958	plural\$4 or several or few or one or first or second or two or overlap\$4	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 11:29			0
4	BRS	L4	1940 100	2 and 3	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 11:34			0
5	BRS	L5	1940 71	1 and 4	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 11:38			0
6	BRS	L6	4092 675	capacit\$4 or deplet\$4 or conduct\$4 or wir\$4	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 11:39			0
7	BRS	L7	1400 55	5 and 6	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 11:41			0
8	BRS	L8	6902 771	decreas\$4 or minim\$4 or lower or current or dark or leakage or photon or gain or convers\$4	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 11:42			0
9	BRS	L9	1335 17	7 and 8	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 11:44			0
10	BRS	L11	3437 075	epitaxial or silicon or logic or insul\$4 or dielectric or thin or implant\$ or ion	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 11:47			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Error
11	BRS	L12	105800	9 and 11	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 11:54			0
12	BRS	L13	20892	12 and (overlap\$4)	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 11:56			0
13	BRS	L14	15422	13 and (logic or circuit\$4)	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 11:57			0
14	BRS	L15	10234	14 and (anneal\$4 or thermal\$4 or heat\$4)	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 11:58			0
15	BRS	L16	9502	15 and (LDD or (light\$4 or dop\$4 or drain))	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 11:59			0
16	BRS	L17	7718	16 and (convert\$4 or multi or layr or spacer or interlayer)	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 12:03			0
17	BRS	L18	7623	17 and (densit\$4 or dopant or concent\$4 or less or higher or heav\$4)	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 12:05			0
18	BRS	L19	95992	CMOS	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 12:06			0
19	BRS	L20	1934	18 and 19	USP AT; US-P GPU B; EPO; JPO; DER WEN T;	2003/10/2 9 12:06			0